

DPW



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

-----ooo:0:ooo-----

Application of :  
Hirofumi HARADA :  
Serial No. 10/812,443 : Group Art Unit: 2823  
Filed: March 30, 2004 : Examiner- Fernando L. Toledo  
For: METHOD OF MANUFACTURING A :  
VERTICAL MOS TRANSISTOR  
(As Amended) : Docket No. S004-5253

-----ooo:0:ooo-----

Mail Stop AMENDMENT  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE**

S I R:

In response to the Office Action dated October 31, 2005, applicant preliminarily amends his application as follows:

**ADDITIONAL FEES:**

No additional fees are believed required in connection with this response; however, should it be determined that a fee is due, authorization is hereby given to charge any such fee to our Deposit Account No. 01-0268.

**MAILING CERTIFICATE ON PAGE 26**

**IN THE TITLE:**

**The Title of the Invention has been amended as follows:**

METHOD OF MANUFACTURING A VERTICAOL VERTICAL MOS  
TRANSISTOR